

Appl. No.: 10/014,943  
Amdt. dated May 21, 2004  
Reply to Office action of March 24, 2004

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently amended) A computer system, comprising:  
a host computer including a CPU coupled to memory, wherein the memory stores host-specific information; and  
a management device coupled to said host, wherein at least a portion of said host-specific information is stored in the management device during a boot process of the host computer and wherein the management device uses the host-specific information is operable to manage a function for that the host computer would otherwise manage using the host-specific information.
2. (Original) The computer system of claim 1 wherein said memory comprises non-volatile memory.
3. (Original) The computer system of claim 2 wherein said memory comprises volatile memory.
4. (Previously presented) The computer system of claim 1 wherein said management device comprises a subsystem of the host computer.
5. (Previously presented) The computer system of claim 4 wherein the host specific information includes a signature which identifies the information whereby the management device locates and transfers said host specific information.
6. (Canceled).
7. (Canceled).

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8. (Previously presented) The computer system of claim 1 wherein said management device includes a CPU that uses the host specific information to control a function for the host computer.

9. (Previously presented) The computer system of claim 1 wherein the management device uploads the host specific information during a power on self test of the host computer.

10. (Previously presented) The computer system of claim 4 wherein said management device uses said host specific information to provide management functionality for the host computer when the host computer is in a low power state.

11. (Previously presented) The computer system of claim 10 wherein the host specific information includes a signature which identifies the information and said management device searches for said signature to find said host specific information.

12. (Canceled).

13. (Canceled).

14. (Previously presented) The computer system of claim 10 wherein said management device includes a CPU.

15. (Previously presented) The computer system of claim 10 wherein said management device operates from an auxiliary power source that is available even if the host computer is off.

16. (Previously presented) The computer system of claim 10 wherein the management device uploads the host specific information during power on self test of the host.

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17. (Currently amended) A logic unit sub-system, comprising:  
a CPU;  
memory coupled to said CPU;  
wherein said logic unit sub-system is adapted to couple to a host computer system and store a table containing host computer information in the memory during a power on self test of the host computer system whereby the logic unit sub-system uses the table to manage a function ~~for~~that the host computer system would otherwise manage.
18. (Currently amended) The logic unit sub-system of claim 17 wherein said logic unit sub-system comprises management logic which manages a function for the host computer system when the host computer is in a low power state.
19. (Currently amended) The logic unit sub-system of claim 18 wherein the host computer information includes a signature which identifies the information and said logic unit sub-system searches for said signature to find said table containing host computer information.
20. (Currently amended) The logic unit sub-system of claim 19 wherein the logic unit sub-system is configured to request a CPU in the host computer system to coordinate the transfer of the table to the logic unit sub-system.
21. (Currently amended) The logic unit sub-system of claim 19 wherein the logic unit sub-system uploads the table without the involvement of a CPU of the host computer system.
22. (Currently amended) The logic unit sub-system of claim 17 wherein the logic unit sub-system uploads the table during a power on self test event as a subsystem of the host computer.

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23. (Currently amended) The logic unit sub-system of claim 17 wherein said logic unit sub-system operates from a different power source than the host computer system and said logic unit can be powered on even if the host computer system is powered off.

24. (Currently amended) A method of operating a logic unit coupled to a host computer, comprising:

searching for host computer specific information during a boot process of the host computer;

upon finding said information, storing said information in a memory of the logic unit ; and

using the information during the operation of the logic unit to independently control a function ~~for~~that the host computer would otherwise control;

wherein said searching and ~~storing~~uploading occur before run-time of the host computer.

25. (Currently amended) The method of claim 24 wherein searching and ~~storing~~uploading before run-time allows a CPU of the host computer to operate without interruption from the logic unit during run-time.

26. (Canceled).

27. (Canceled).

28. (Previously presented) The method of claim 24 wherein storing the computer specific information in a memory of the logic unit comprises storing at least one of an Advanced Configuration and Power Interface ("ACPI") table and a system management basic input/output system ("SMBIOS").

29. (Currently amended) A system, comprising:

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a host computer that has a central processing unit ("CPU") coupled to a peripheral interface and a memory unit that stores an information table; and

a management unit coupled to the peripheral interface of the host computer, the management unit accesses and stores the information table during a boot process of the host computer such that the management unit is operable to carry out a predetermined management responsibility that the host computer would otherwise carry out associated with the information table prior to the host computer reaching a run time.

30. (Previously presented) The system of claim 29 wherein the management unit comprises a battery power supply such that the management unit is operable when the host computer is in a low power state.

31. (Previously presented) The system of claim 29 wherein the management unit comprises:

a ROM memory that stores computer readable instructions for accessing and storing the information table; and

a processor that executes the computer readable instructions.

32. (Previously presented) The system of claim 31 wherein the processor requests the CPU to transfer a copy of the information table to a memory of the management unit.

33. (Previously presented) The system of claim 31 wherein management logic of the management unit is configured to control the host computer's peripheral interface and is operable to read the information table from the host computer's memory unit such that the CPU is not needed to access and store the information table.